

Ellsworth
Arroyo3 PVT Schematics

PCB Version : 6050A2516401 XTAL LIST

Table with 2 columns: CONTENTS, SHEET. Rows include: 01: TABLE OF CONTENTS (1), 02: SYSTEM BLOCK DIAGRAM (2), 03: CLK/SMBUS/RESET MAP (3), 04: AMD FT1 (1/2) (4), 05: AMD FT1 (2/2) (5), 06: DDR3 SODIMM A-1 CHANNEL (6), 07: DDR3 SODIMM A-2 CHANNEL (7), 08: SCALAR ANX3110 (8), 09: AMD HUDSON D3L (1/4) (9), 10: AMD HUDSON D3L (2/4) (10), 11: AMD HUDSON D3L (3/4) (11), 12: AMD HUDSON D3L (4/4) (12), 13: AUDIO ALC269-VC (13), 14: LAN RTL8175E-CG/LED (14), 15: CR/HDD/ODD/WEBCAM/D-MIC (15), 16: USB/MINI PCI-E/FAN (16), 17: USB3.0 CONN (17), 18: EC IT8519E (18), 19: DEBUG CIRCUIT (19), 20: SYSTEM +3V/+5V (20), 21: +VDDIO\_MEM/MEM\_VTT/DC-IN (21), 22: +1.8V/+12V/+1.2V/+1.1V/DIS (22), 23: +1.1V\_S3/+1.05V (23), 24: VDDCR\_CPU/VDDCR\_NB (24), 25: STACK UP 6L (25), 26: SCREW HOLE/SILK SCREEN (26), 27: Power Sequence Diagram (27), 28: POWER SEQUENCE (28), 29: GPIO TABLE (29), 30: HISTORY (30).

Table with 6 columns: PARTS, DIP/SMT, Frequency, PPM, CL. Rows include: 1 X2 SMT 25MHz ±20 12pF/15pF, 2 X3 SMT NC/25MHz ±20 10pF/10pF, 3 X4 SMT 32.768KHz ±20 10pF/10pF, 4 X5 SMT 25MHz ±20 12pF/12pF, 5 X6 SMT NC/32.768KHz ±20 12pF/12pF.

USB LIST

Table with 3 columns: PORT, NET NAME, FUNCTION. Rows include: USB\_HSD0P/N, USB0\_DEBUG\_P/N, REAR IO; USB\_HSD1P/N, USB1P/N, REAR IO; USB\_HSD2P/N, USB2P/N, REAR IO; USB\_HSD3P/N, USB3P/N, REAR IO; USB\_HSD5P/N, USB5\_WLAN\_P/N, WLAN/BT; USB\_HSD8P/N, USB8\_WEBCAM\_P/N, WEBCAM; USB\_SS\_TX0P/N, USB3.0\_TX0P/N, SIDE IO; USB\_SS\_TX1P/N, USB3.0\_TX1P/N, SIDE IO.

SATA LIST

Table with 3 columns: PORT, NET NAME, FUNCTION. Rows include: SATA\_TX0P/N, SATAA0\_TXP/N, SATA HDD; SATA\_RX0P/N, SATAA0\_RXP/N, SATA HDD; SATA\_TX1P/N, SATAA1\_TXP/N, SATA ODD; SATA\_RX1P/N, SATAA1\_RXP/N, SATA ODD.

PCI-E LIST

Table with 3 columns: PORT, NET NAME, FUNCTION. Rows include: GPP\_TX3P/N, PCIE\_WLAN\_TXP/N, WIRELESS LAN; GPP\_RX3P/N, PCIE\_WLAN\_RXP/N, WIRELESS LAN; GPP\_TX1P/N, PCIE\_LAN\_TXP/N, LAN; GPP\_RX1P/N, PCIE\_LAN\_RXP/N, LAN; GPP\_TX2P/N, PCIE\_CARD\_TXP/N, CARDREADER; GPP\_RX2P/N, PCIE\_CARD\_RXP/N, CARDREADER.

POWER STATES

Table with 8 columns: STATE, SIGNAL, VOTAGE, S0, S3, S5, S5 WARM, G3, REMARK. Rows include: FCH\_SLP\_S3#, FCH\_SLP\_S5#, S5\_PWR\_ON, +VBAT\_IN, +VIN, +3V\_ALW/+5V\_ALW, +3V\_S5, +1.1V\_S5, +5V\_S3, +3V\_S3, +VDDIO\_MEM, +1.1V\_S3, +MEM\_VTT, +3V\_S0/+5V\_S0, +1.8V\_S0, +12V\_S0, +1.1V\_S0, +1.5V\_S0, +1.2V\_S0, +1.05V\_S0, +VDDCR\_CPU, +VDDCR\_NB.

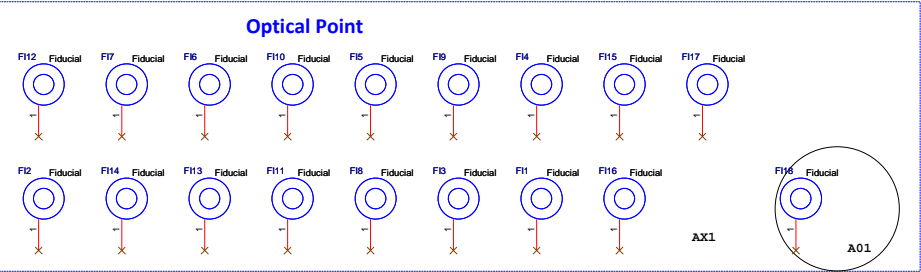
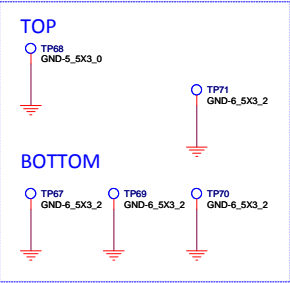
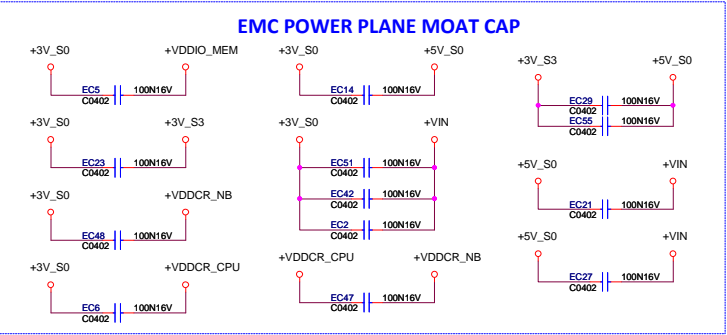
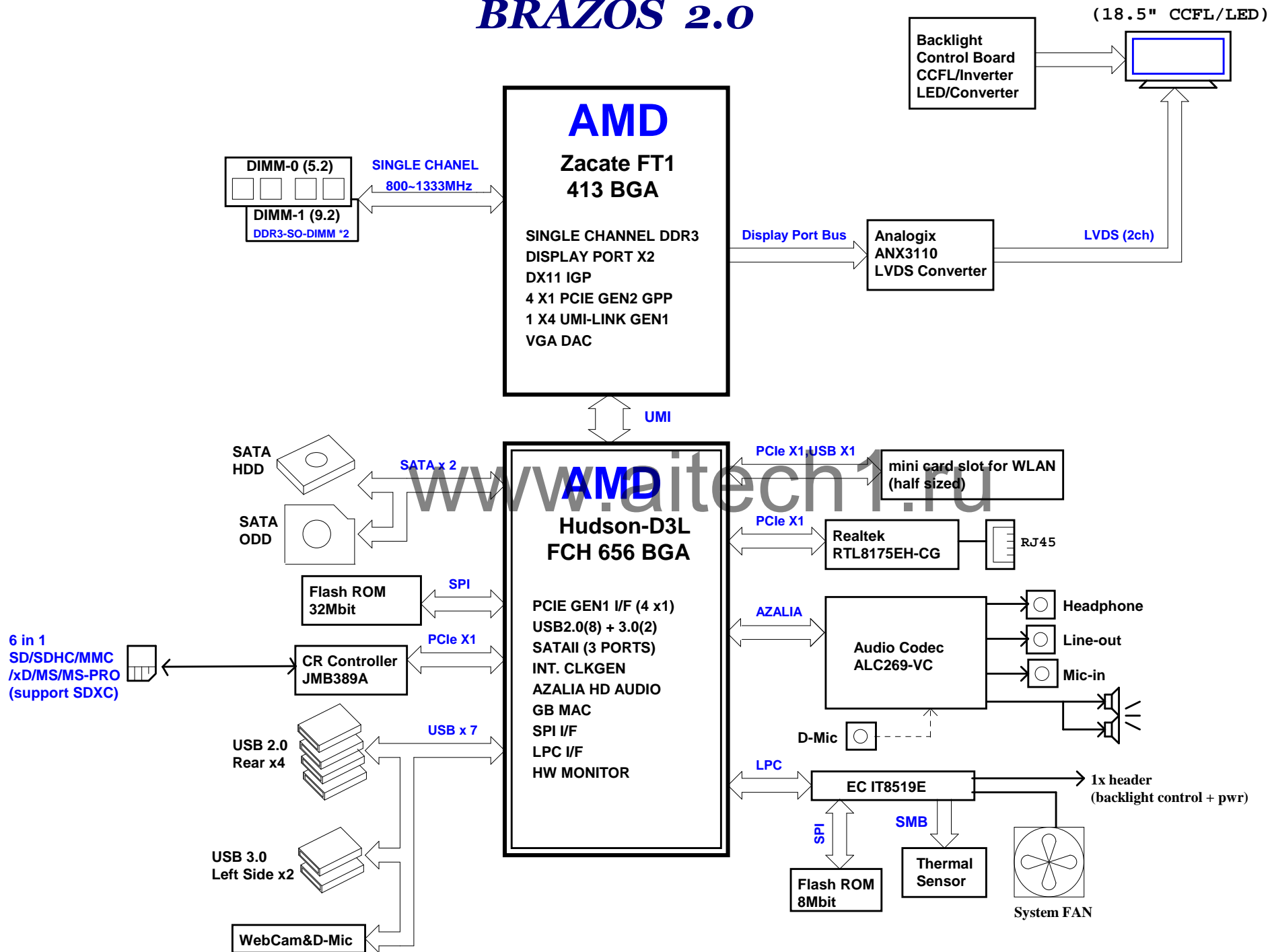


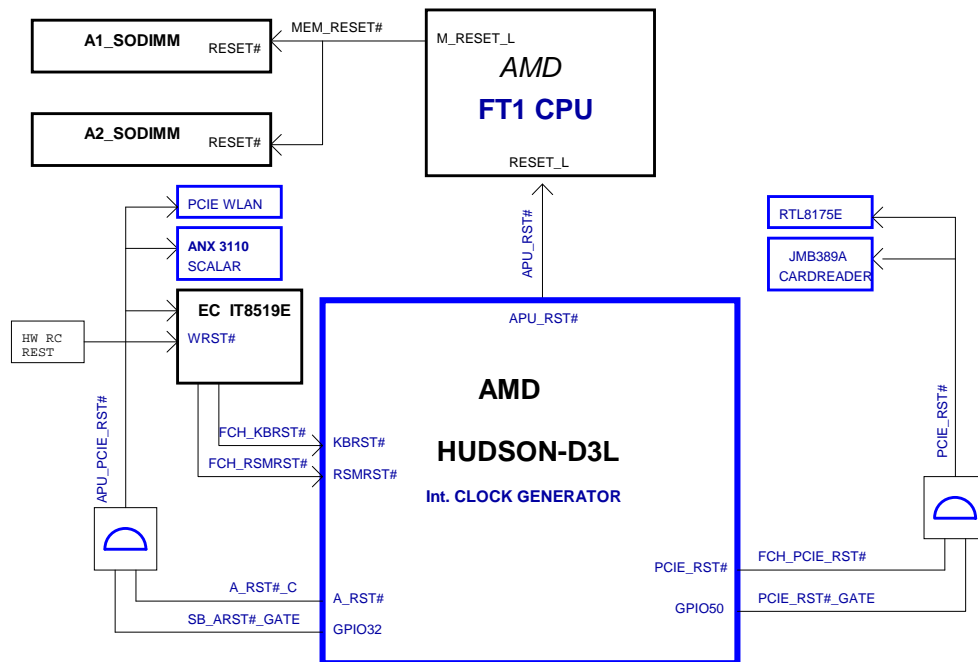
Table with 4 columns: TPV (Top Victory Electronics Co., Ltd.), OEM MODEL, HP, Arroyo3, Size, C. Rows include: 1, Arroyo3, 6050A2516401, 1 of 30.

# BRAZOS 2.0

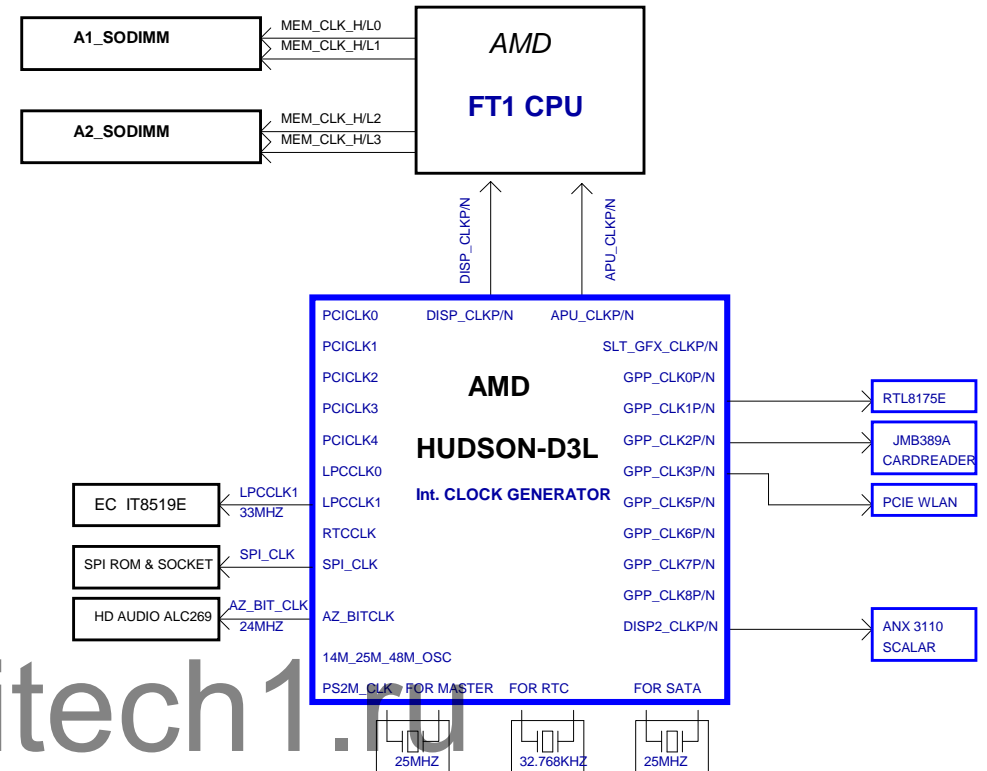


TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_ArroyoS	Size	C
TPV MODEL	ArroyoS	Rev	AG2	
PCB NAME	0050A2516401	Sheet	2 of 30	備註 <備註>
Date	Tuesday, May 22, 2012			

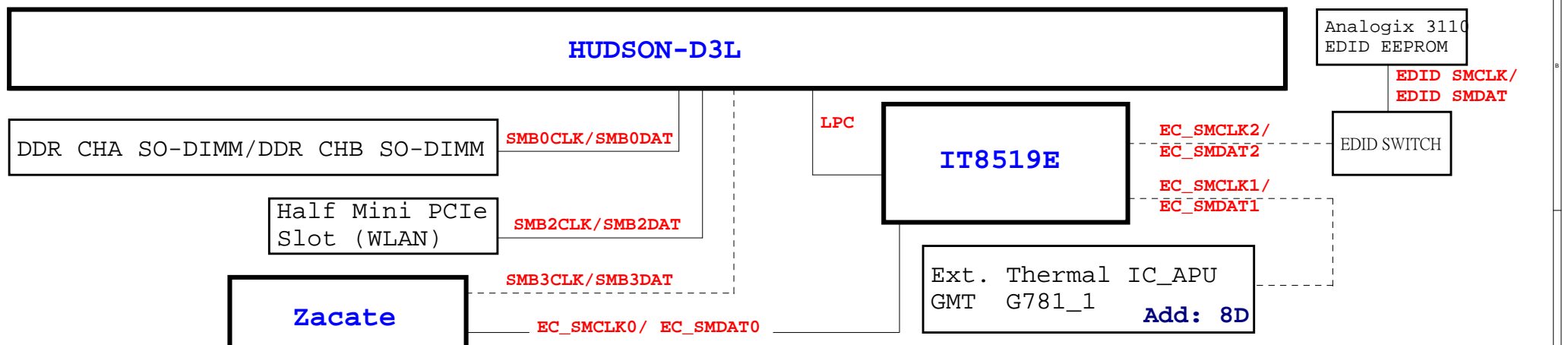
## RESET Block Diagram



## INTERNAL CLOCK MODE

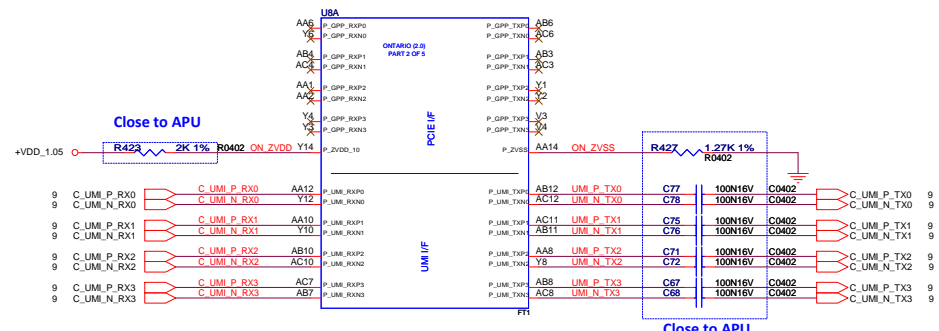


## SM Bus MAP

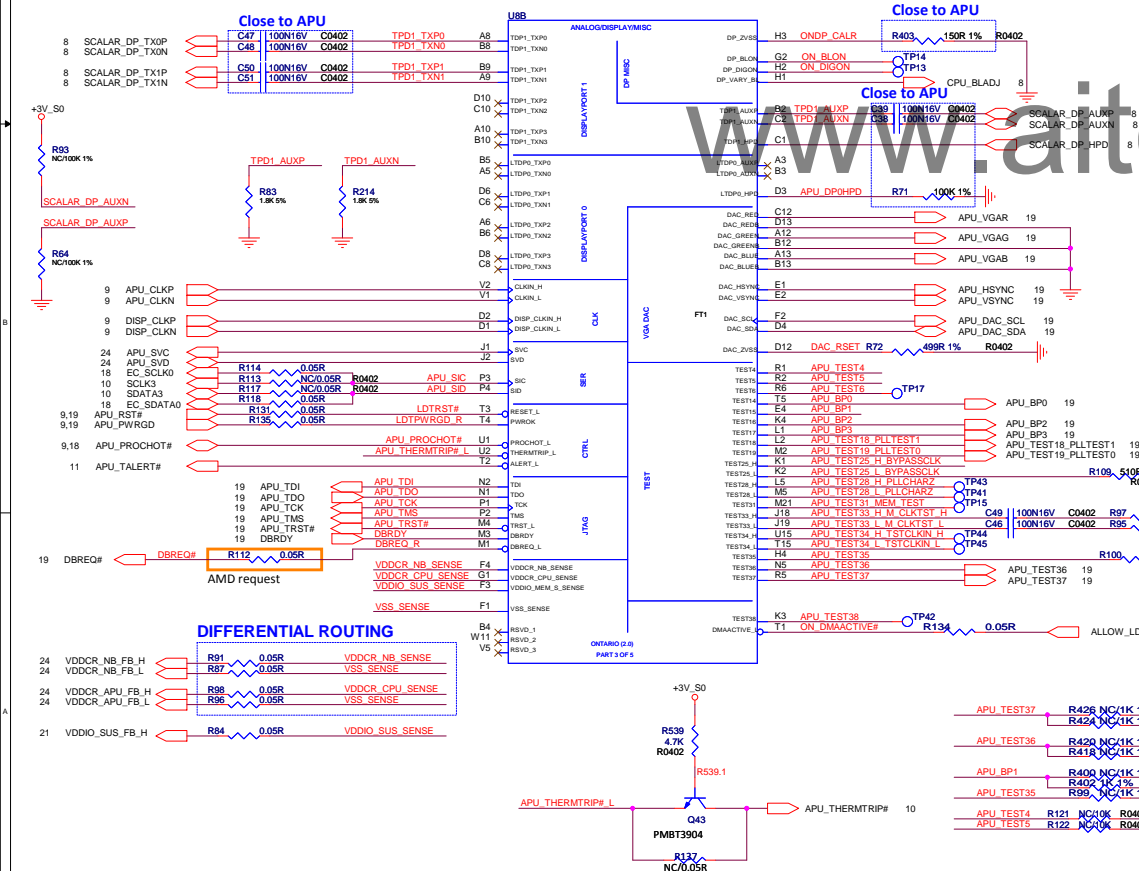


TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
TPV MODEL	Arroyo3		Rev	A02
PCB NAME	6050A2516401		Sheet	3 of 30
Date	Tuesday, May 22, 2012		備註	<備註>

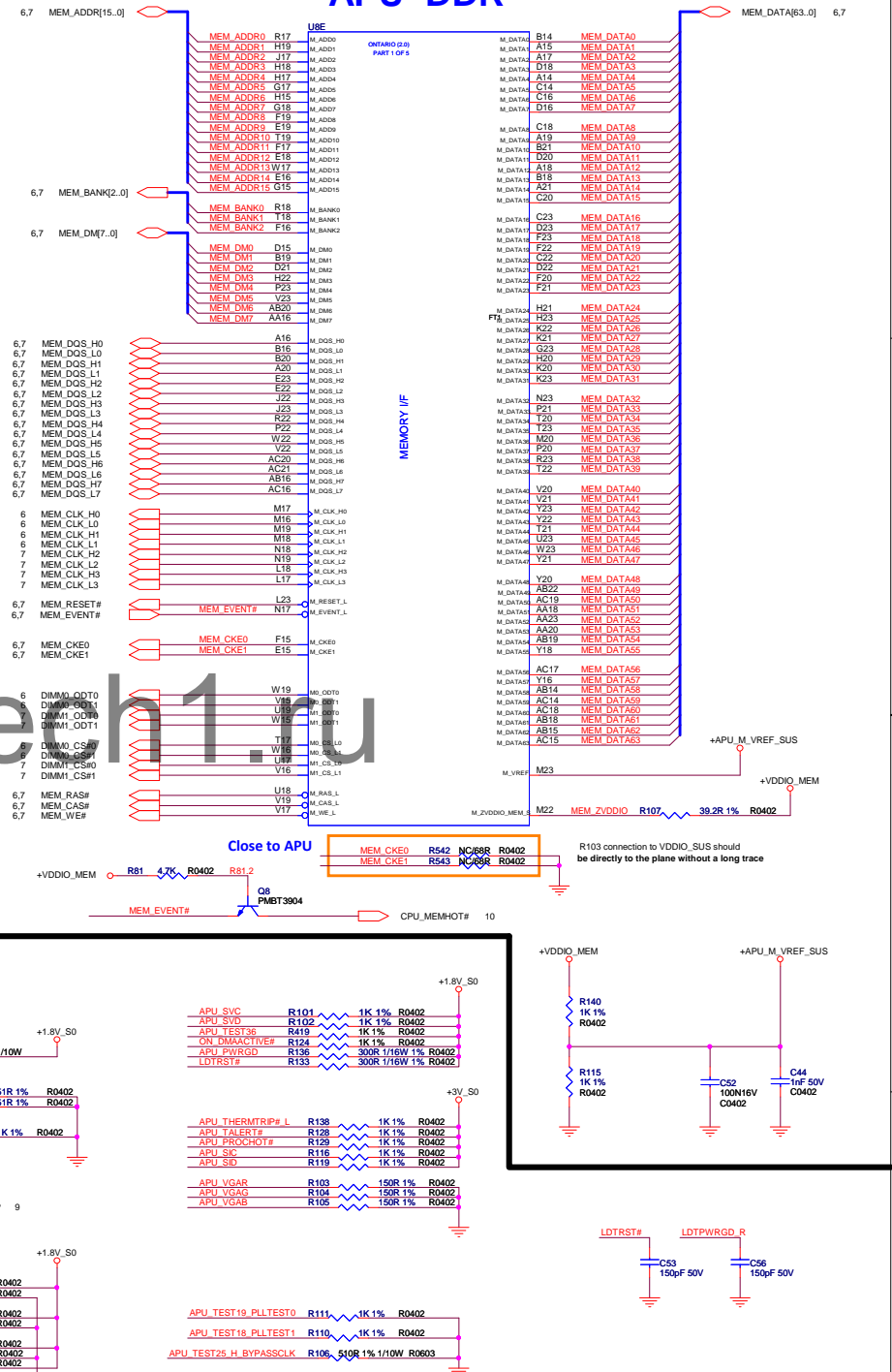
## UMI / PCIE



**DP / VGA / CLK / CTRL / SENSE**

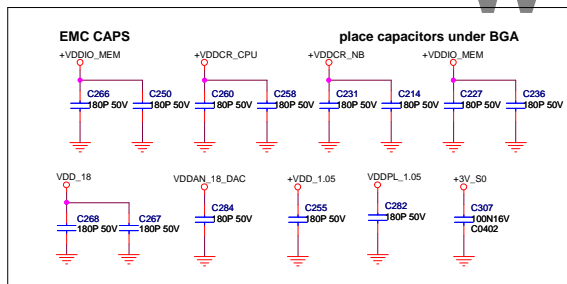


**APU DDR**

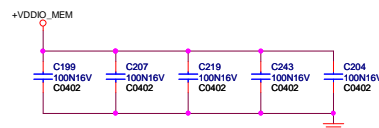
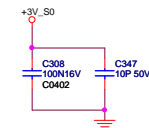
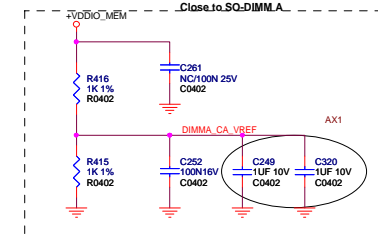
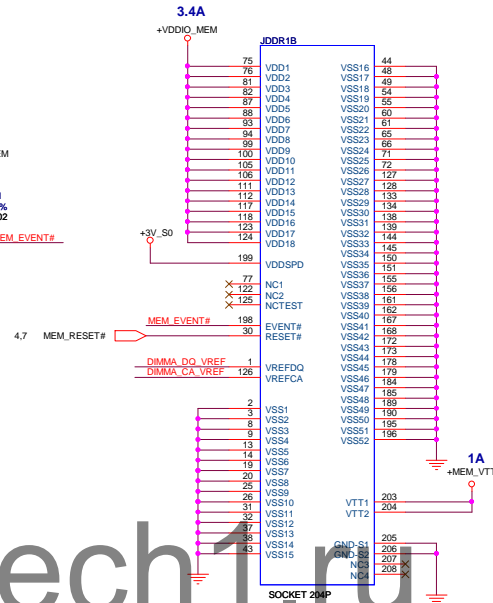


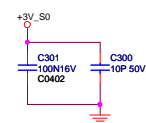
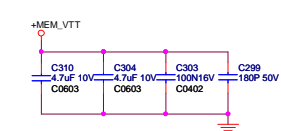
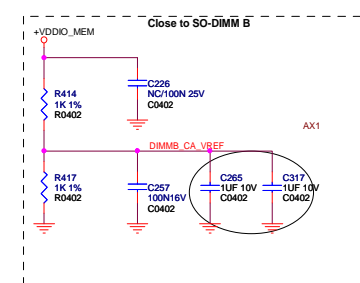
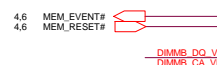
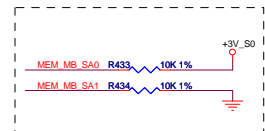
TPV (Top Victory Electronics Co., Ltd.)		OEM MODEL	HP_Arroyo3	Size	C
線路圖編號 1		TPV MODEL	Arroyo3	Rev	A02
Key Component	TABLE OF CONTENTS	PCB NAME	6050A2516401	備註	<備註>
Date	Tuesday, May 22, 2012	Sheet	4 of 30		

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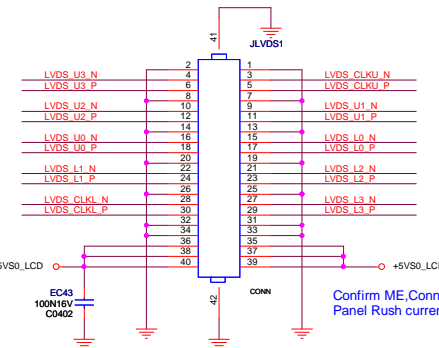
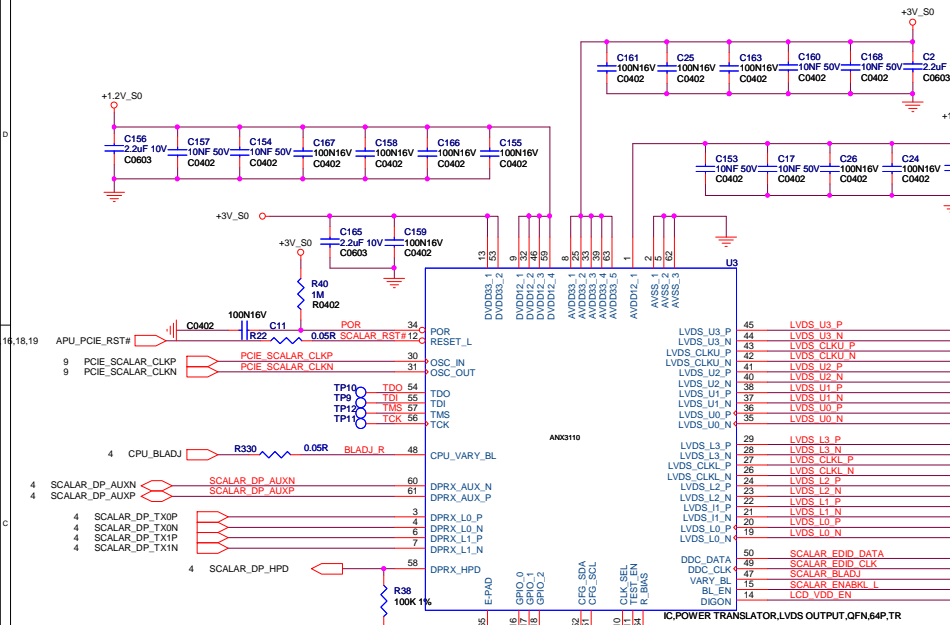
TPV (Top Victory Electronics Co., Ltd.)		OEM MODEL	HP_Arrayo3	Size	C
線路圖編號	1	TPV MODEL	Arrayo3	Rev	A02
Key Component	TABLE OF CONTENTS	PCB NAME	6050A2516401		
Date	Taiwanese May 22, 2012	Sheet	1 of 20	備註	<備註>



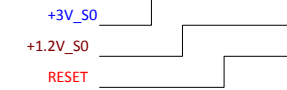
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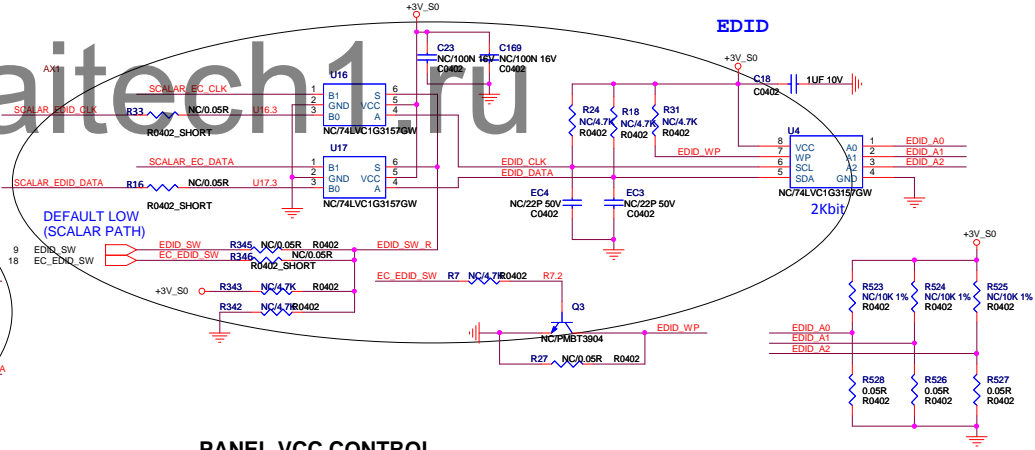
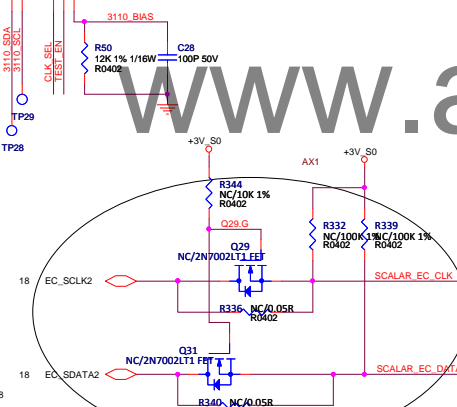
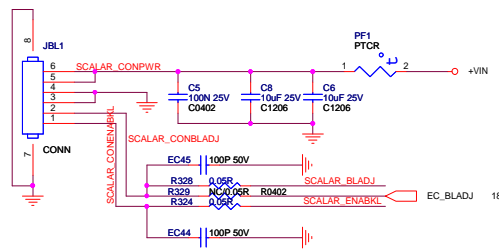
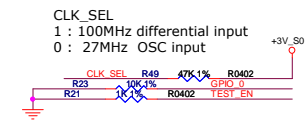
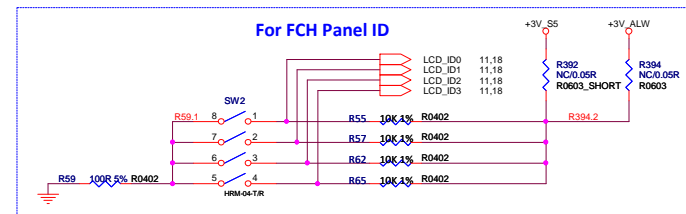
# ANX3110



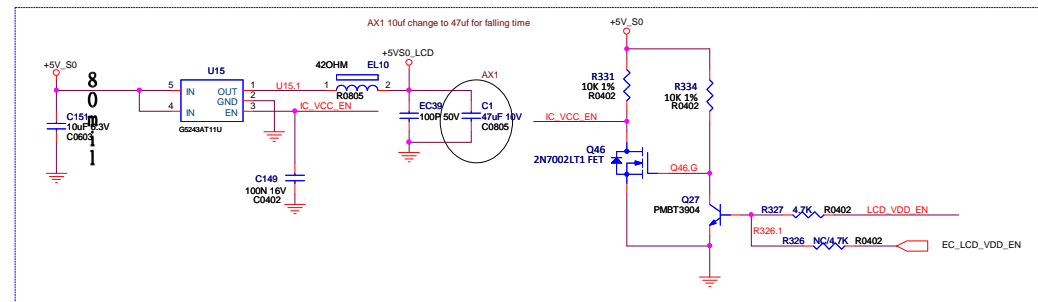
## ANX3110 Power Sequence



	ID0	ID1	ID2	ID3
	0	0	0	0
	0	0	0	1
	0	0	1	0
	0	0	1	1
	0	1	0	0
	0	1	0	1
	0	1	1	0



### PANEL VCC CONTROL



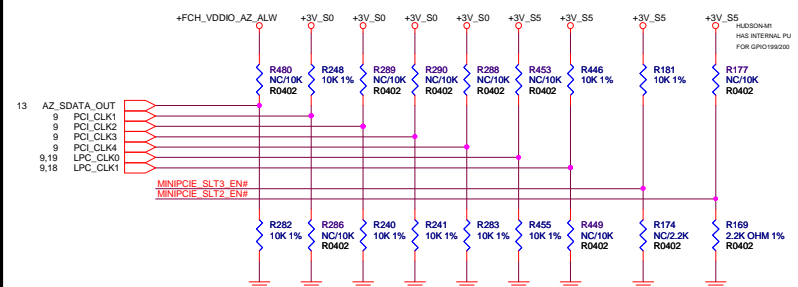
TPV (Top Victory Electronics Co., Ltd.)		OEM MODEL	HP_Arroyo3	Size	C
	1	TPV MODEL	Arroyo3	Rev	A02
Key Component	TABLE OF CONTENTS	PCB NAME	6050A2516401		
Date	Tuesday, May 22, 2012	Sheet	8 of 30	<	>



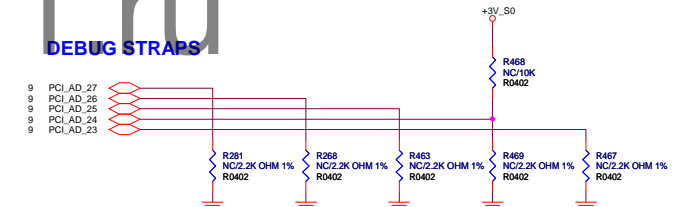


TPV (Top Victory Electronics Co., Ltd.)		OEM MODEL	HP_Arroyo3	Size	C
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Key Component	TABLE OF CONTENTS	PCB NAME	6050A2516401	備註	<備註>
Date	Tuesday 2023-10-10	Sheet	0 of 30		

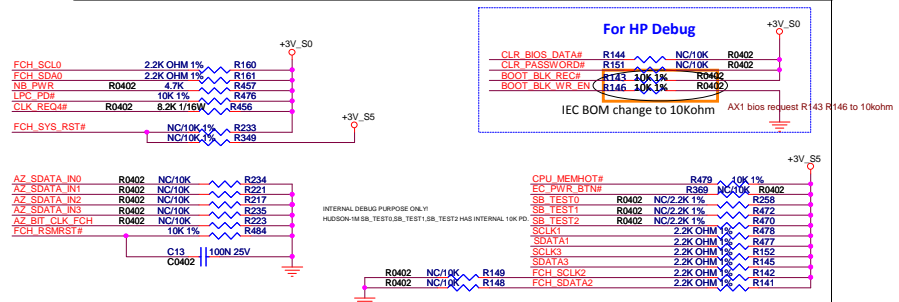
## STRAP PINS



## DEBUG STRAPS

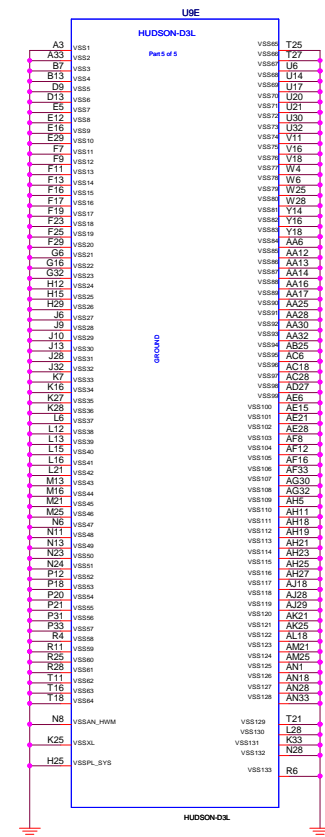
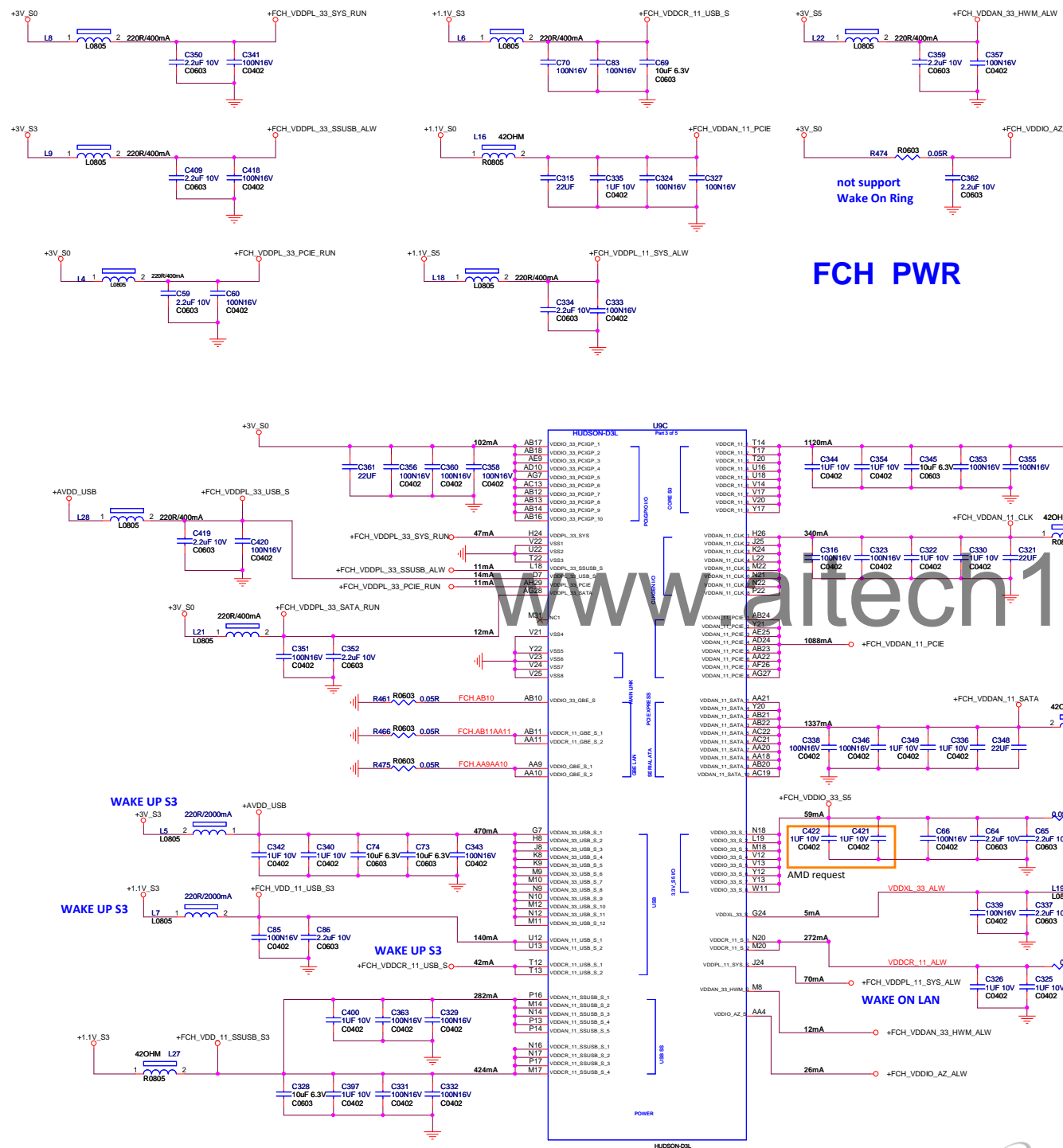


	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL "HIGH"	USE PCI PLL DEFAULT	DISABLE ILA AUTORUN DEFAULT	USE PCI PLL DEFAULT	USE DEFAULT PCI STRAPS DEFAULT	DISABLE PCI MEM BOOT DEFAULT
PULL "LOW"	BYPASS PCI PLL	ENABLE ILA AUTORUN	BYPASS PCI PLL	USE EPROM PCI STRAPS	ENABLE PCI MEM BOOT



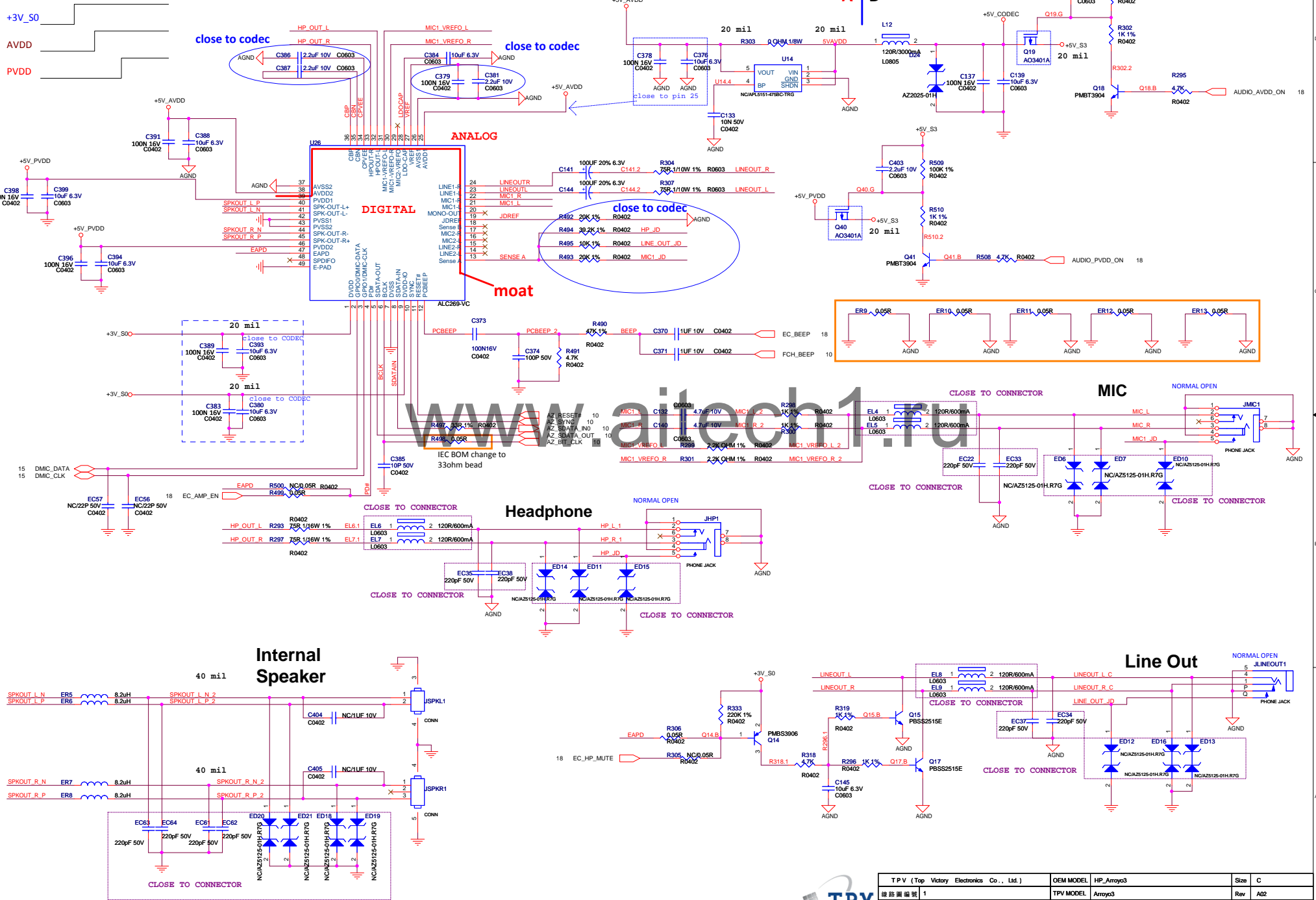
TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arrayo3	Size	C
線路圖編號 1	TPV MODEL	Arrayo3	Rev	A02
Key Component	TABLE OF CONTENTS	PCB NAME	6050A2516401	
Date	Tuesday, May 22, 2012	Sheet	10 of 30	備註 <備註>





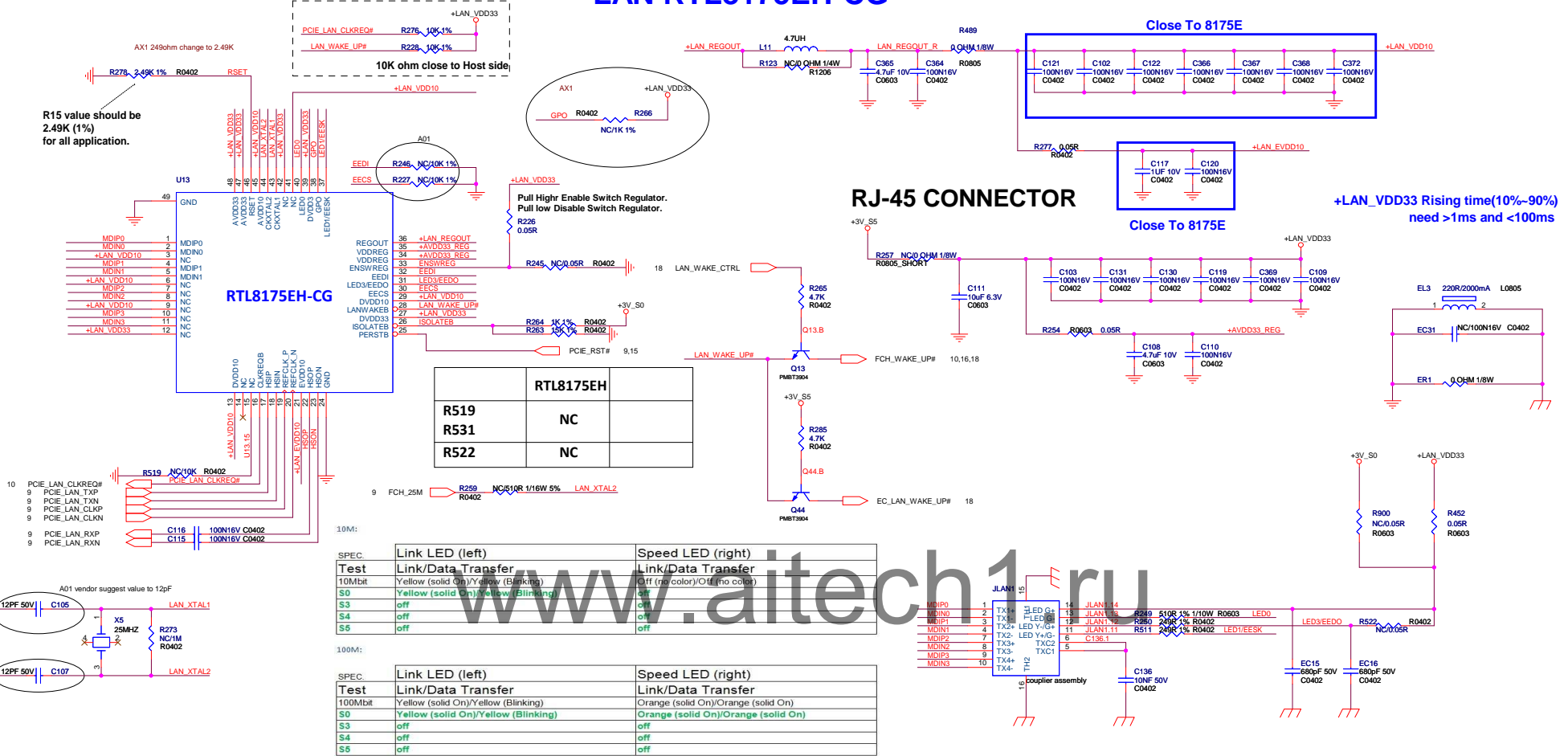
## AUDIO CODEC (ALC269-VC)

## Power Sequence

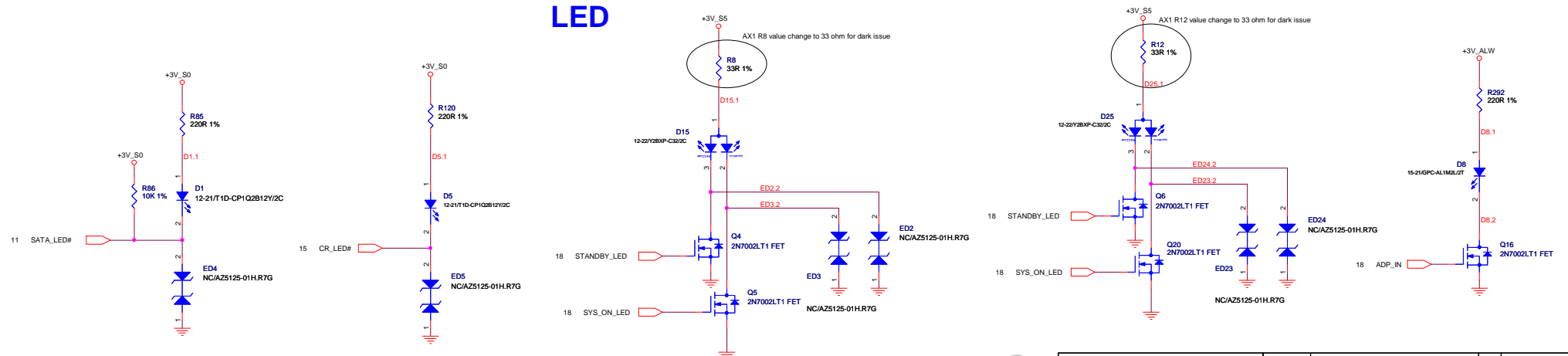


T P V (Top Victory Electronics Co., Ltd.)		OEM MODEL	HP_Array03	Size	C
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Key Component	TABLE OF CONTENTS	PCB NAME	6050A2516401	備註 <備註>	
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# LAN RTL8175EH-CG



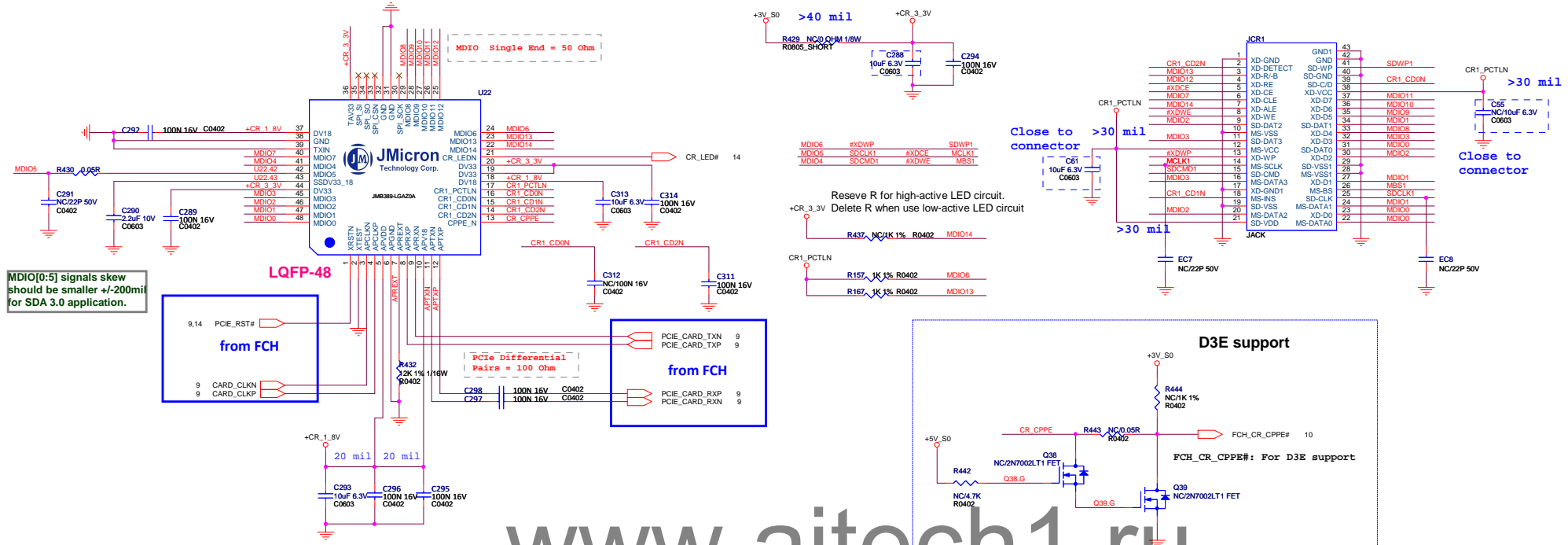
## LED



T P V (Top Victory Electronics Co., Ltd.)		OEM MODEL	HP_Array3	Size	C
線路圖編號	1	TPV MODEL	Array3	Rev	A02
Key Component	TABLE OF CONTENTS	PCB NAME	6050A2516401	備註 <備註>	
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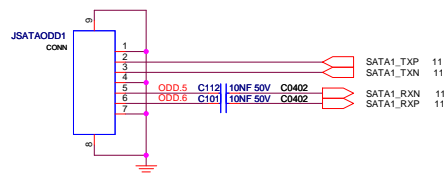
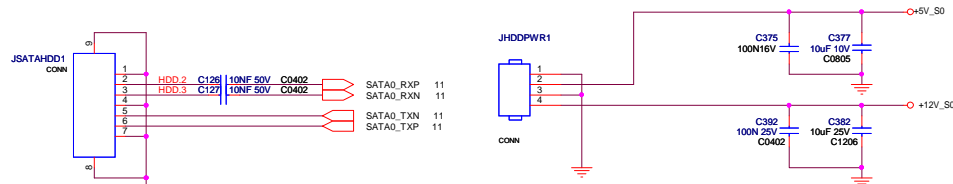


# Card Reader JMB389A



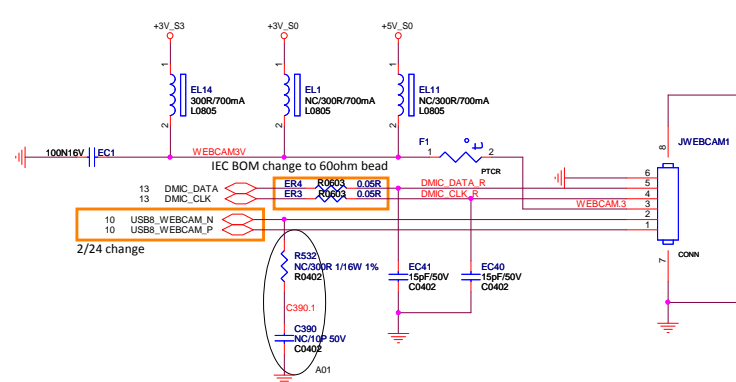
## HDD/ODD

### HDD POWER (3.5")



### ODD POWER

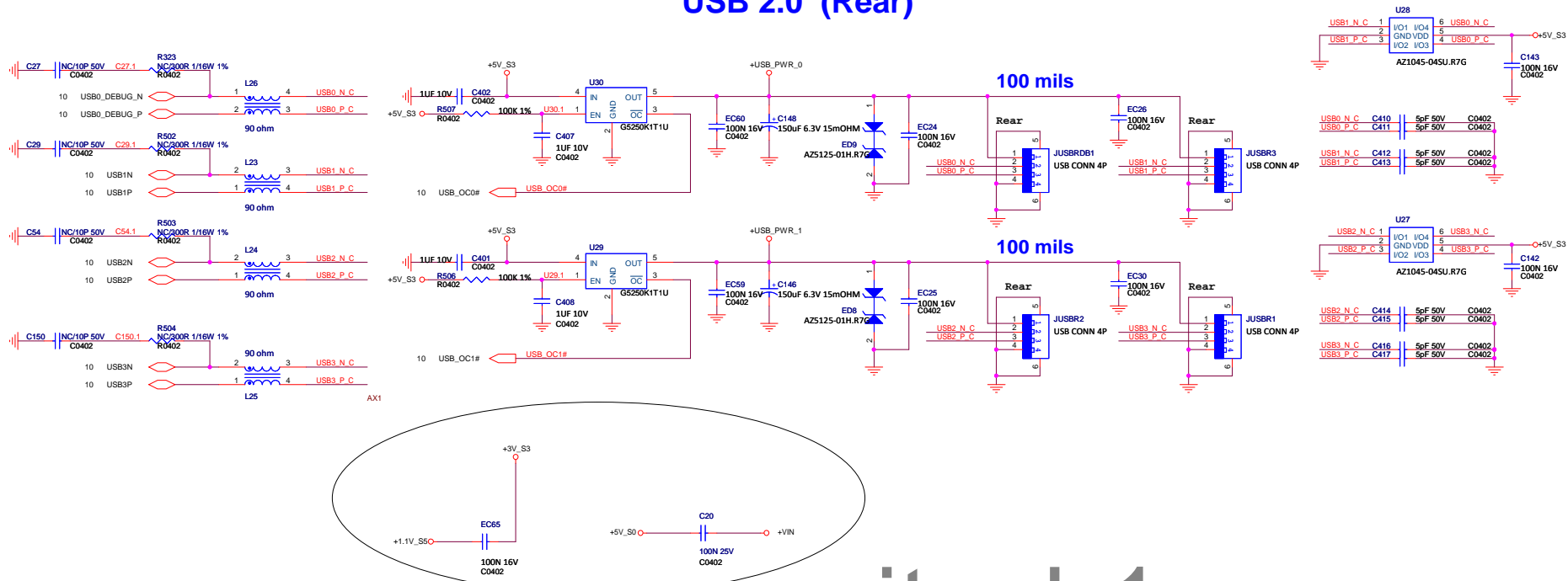
## WEB CAM / D-MIC



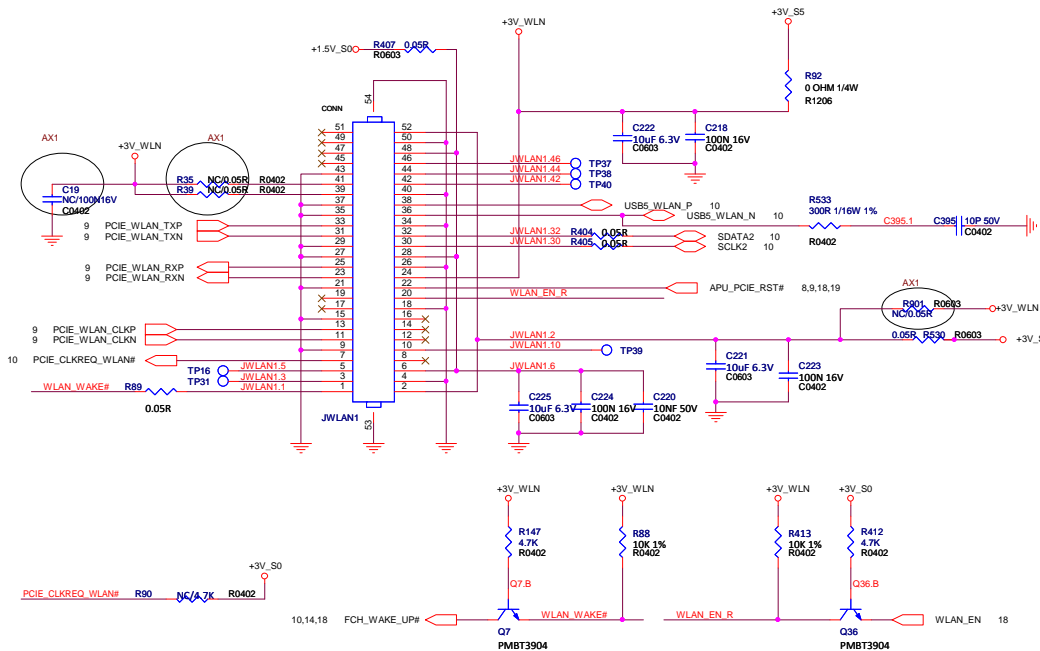
TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
Key Component	TPV MODEL	Arroyo3	Rev	A02
Table of Contents	PCB NAME	6050A2516401	Sheet	15 of 30
Date	Tuesday, May 22, 2012			備註 <備註>



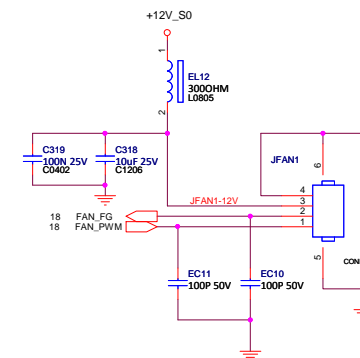
## USB 2.0 (Rear)



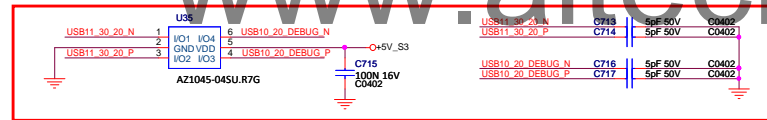
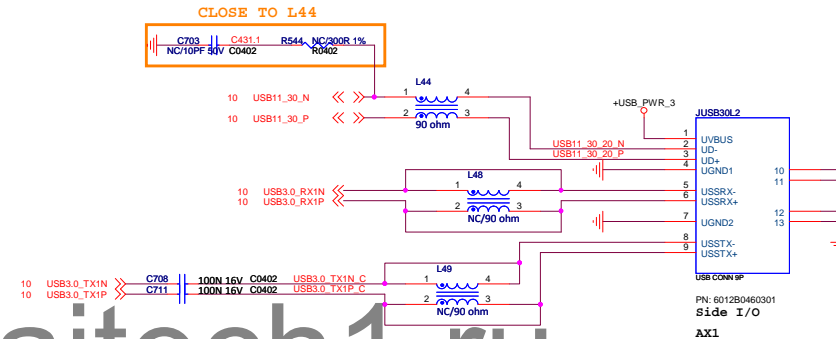
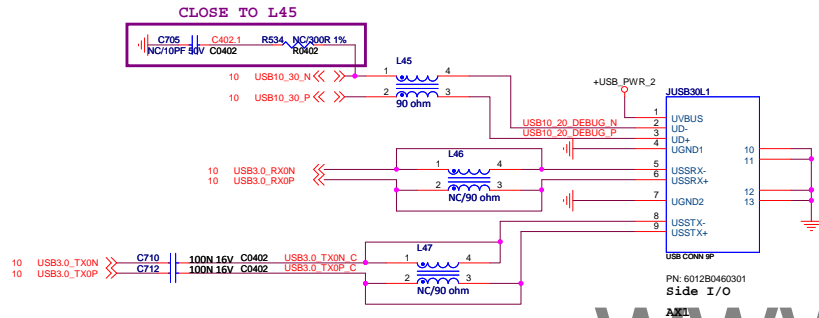
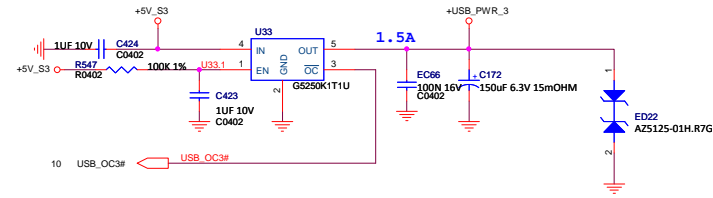
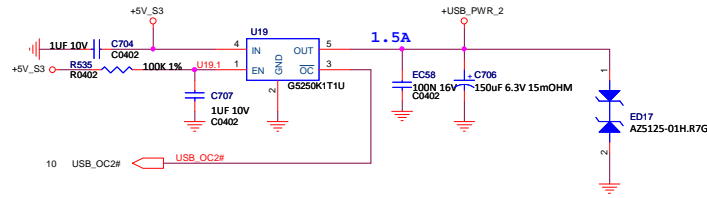
## MINI PCI-E WLAN & BT



## CPU Linear FAN Control



# USB 3.0 (Side IO)



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TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
1	TPV MODEL	Arroyo3	Rev	A02
Key Component	TABLE OF CONTENTS	PCB NAME	6050A2516401	備註
Date	Tuesday, May 22, 2012	Sheet	17 of 30	<備註>

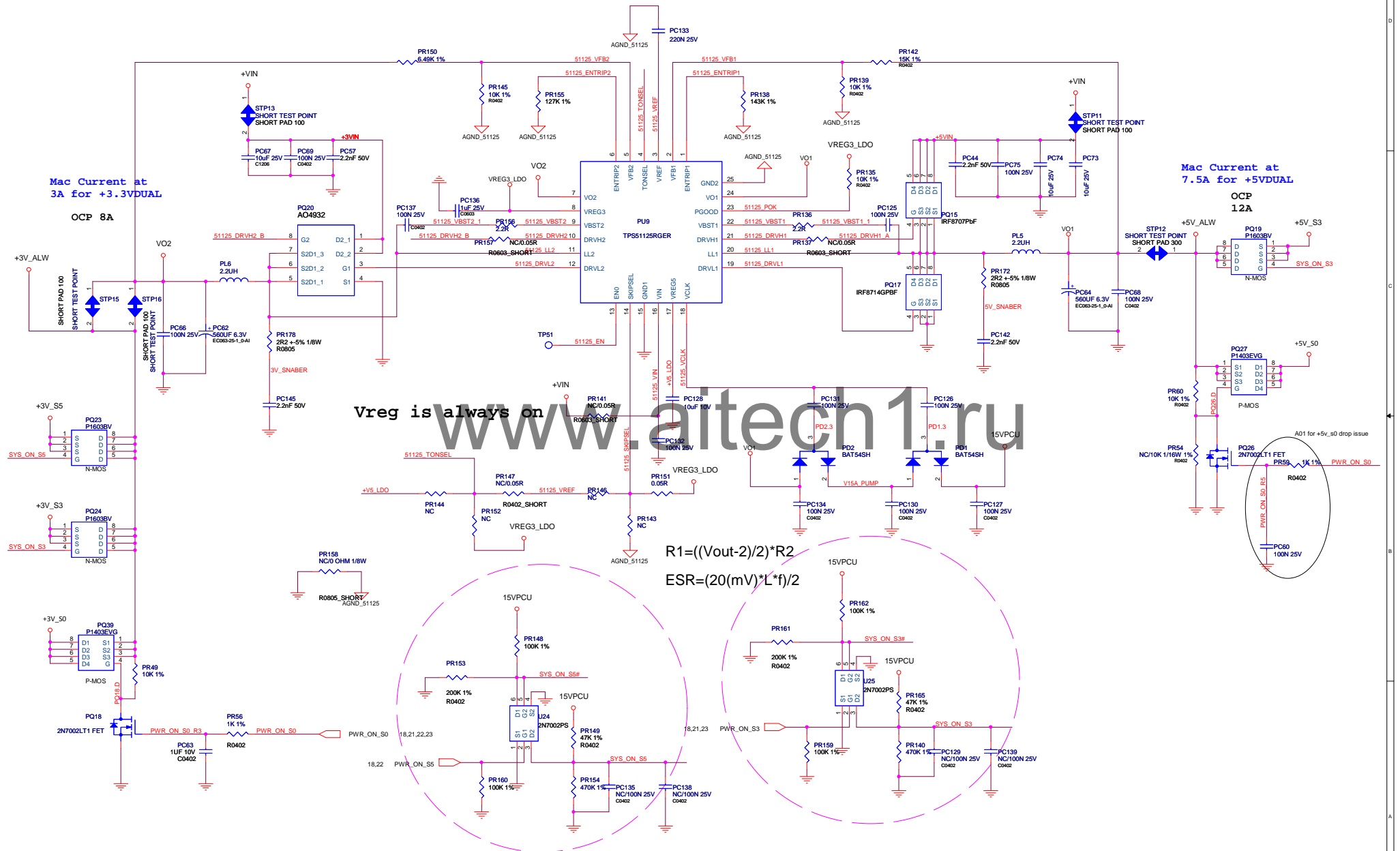
## IT8519



TP V (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arrayo3	Size	C
產品圖編號 1	TPV MODEL	Arrayo3	Rev	A02
Key Component	TABLE OF CONTENTS	PCB NAME	6050A2516401	
Date	Tuesday, May 22, 2012	Sheet	18 of 30	備註 <備註>



# SYSTEM +3V\_ALW/+3V\_S5/+3V\_S3/+3V\_S0 +5V\_ALW/+5V\_S3/+5V\_S0

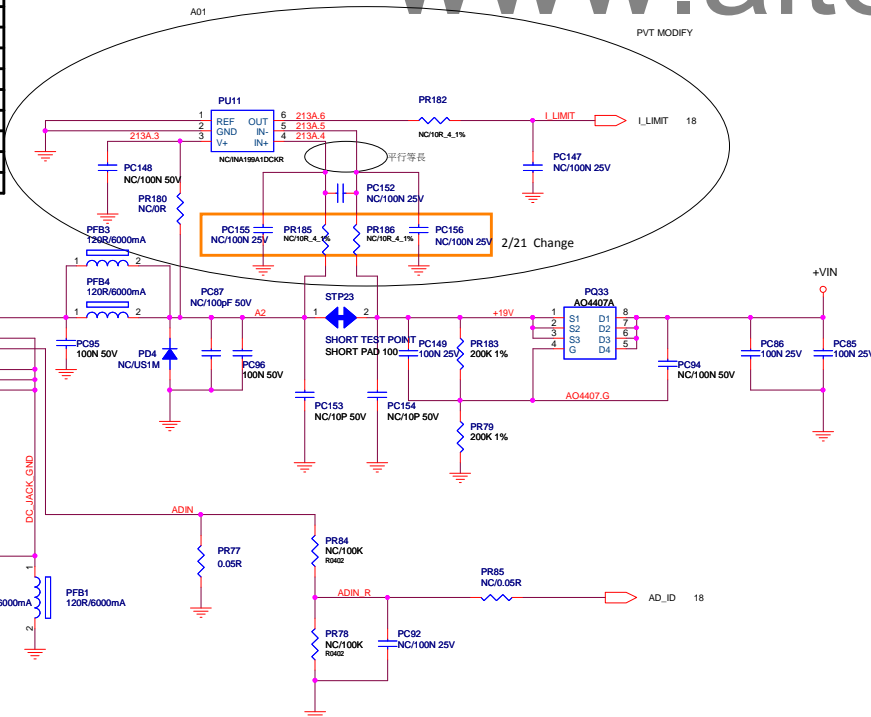


TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
Key Component	TPV MODEL	Arroyo3	Rev	A02
Date	TABLE OF CONTENTS	6050A2516401	備註	<備註>
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# VDDIO\_SUS, MEM\_VTT, DC-IN

1A\*.01ohm\*50=0.5(I\_LIMIT Vo)

TOTAL POWER	I_LIMIT
0W	0V
5W	0.135V
10W	0.273V
15W	0.404V
20W	0.535V
25W	0.673V
30W	0.807V
35W	0.938V
40W	1.075V
45W	1.206V
50W	1.340V
55W	1.472V
60W	1.609V
65W	1.740V
70W	1.875V
75W	2.010V
80W	2.144V
85W	2.276V
90W	2.379V



$$V_{out} = 0.704V * (R1 + R2) / R2$$

$$ESR = L * f / 70$$

Mac Current at 6A for VDDIO\_SUS

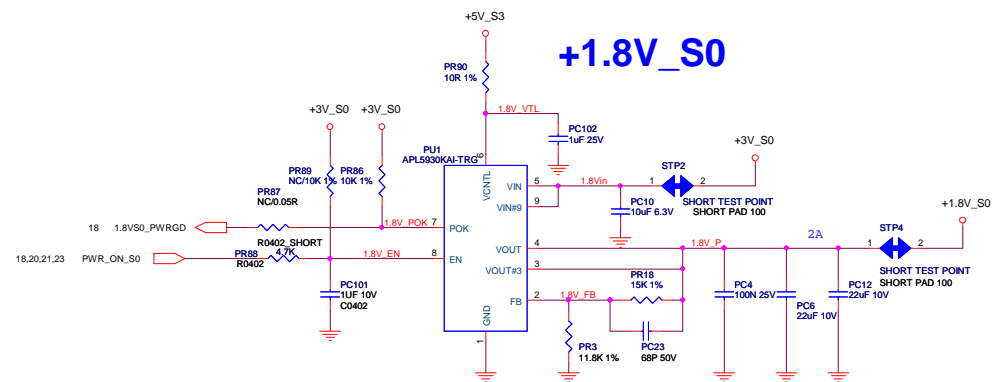
OCP 10A

MEM\_VTT Power

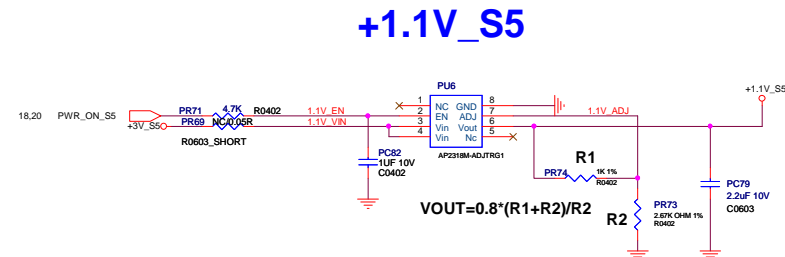
MEM\_VTT\*2:600mA



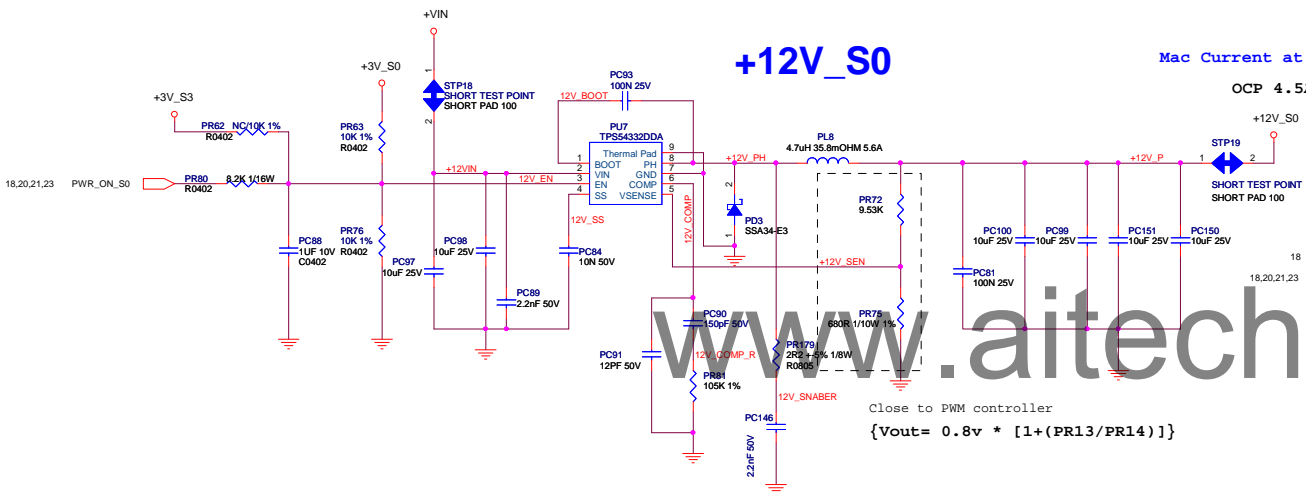
TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
Key Component	TPV MODEL	Arroyo3	Rev	A02
Date	TABLE OF CONTENTS	6050A2516401	Sheet	21 of 30
				備註 <備註>



$$V_{out} = 0.8V * (R1 + R2) / R2$$

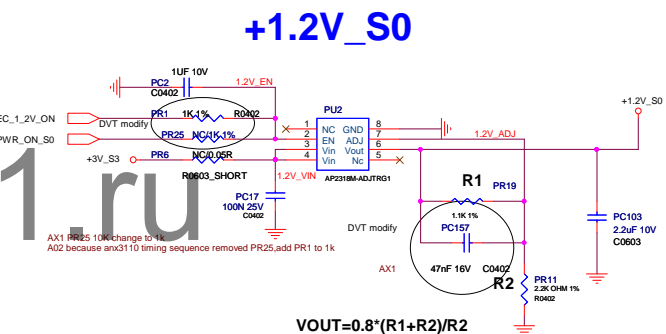


$$V_{OUT} = 0.8 * (R1 + R2) / R2$$



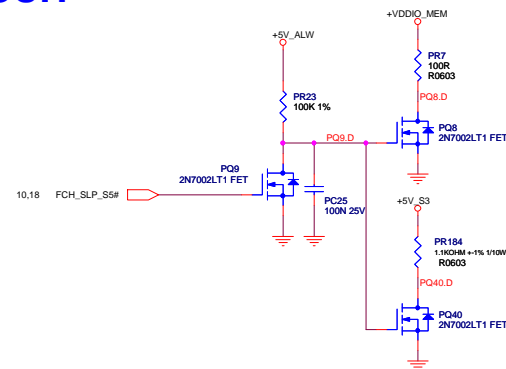
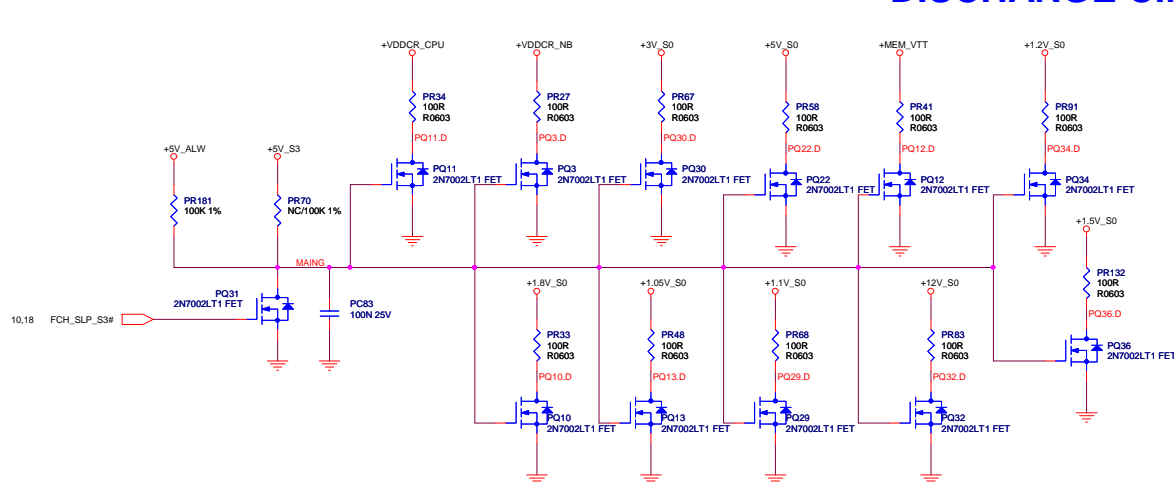
Mac Current at 2.5A for +V12S

OCP 4.5A



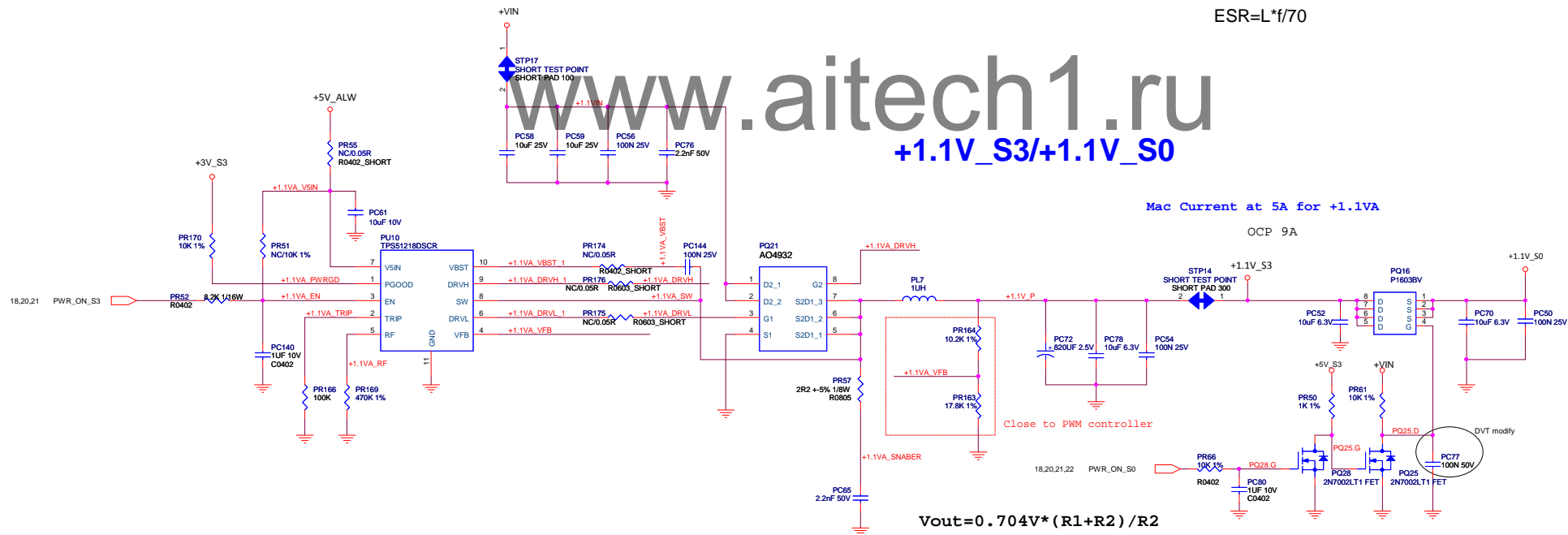
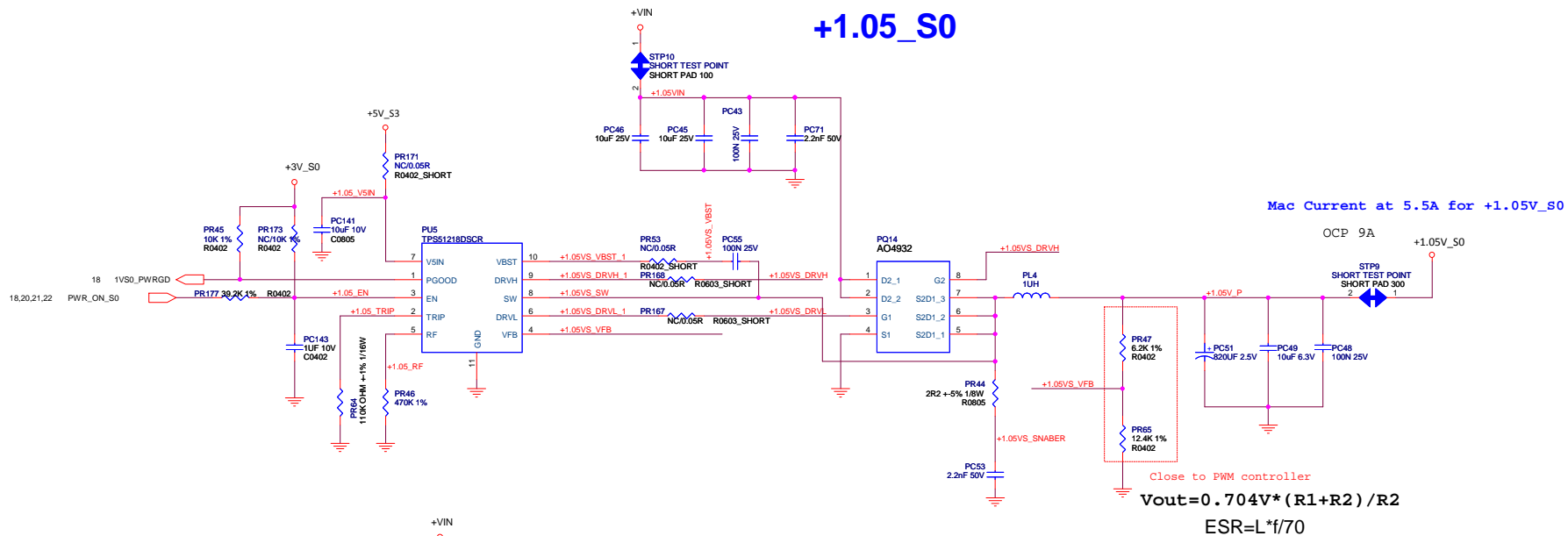
$$V_{OUT} = 0.8 * (R1 + R2) / R2$$

## DISCHARGE CIRCUIT



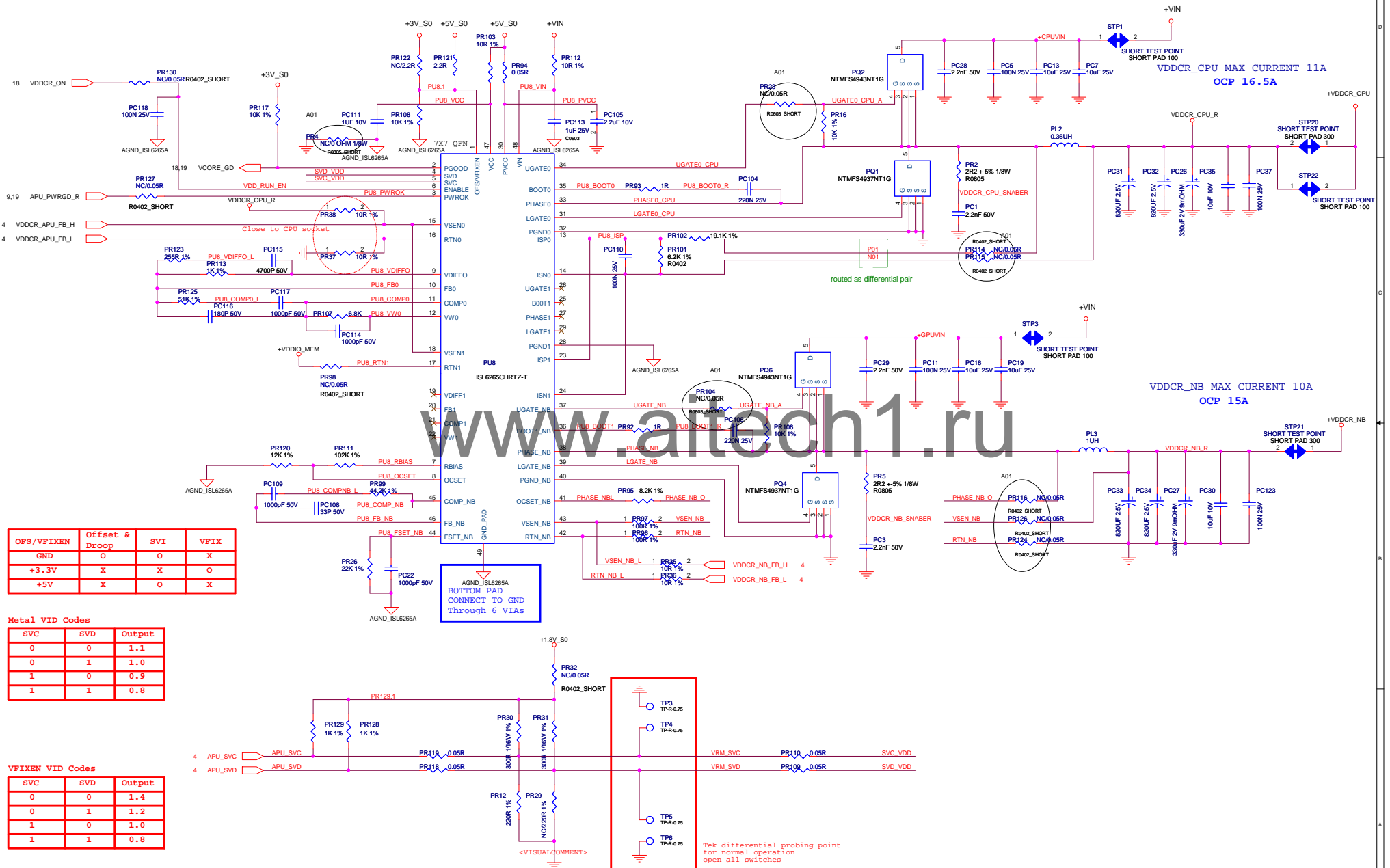
TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
Key Component	TABLE OF CONTENTS	TPV MODEL	Arroyo3	Rev
Date	Tuesday, May 22, 2012	PCB NAME	6050A2516401	A02
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TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
Table of Contents	TPV MODEL	Arroyo3	Rev	A02
Key Component	PCB NAME	6050A2516401	備註	<備註>
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# VDDCR\_CPU & VDDCR\_NB

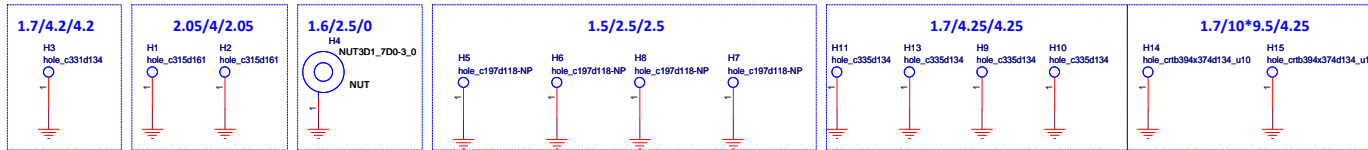


## STACK UP 6L

Stack Up							
PCB Stack Up				Impedance			
Layer	Type	Thickness (mil)					
Top side solder mask		1.00 mils		Single end	理論値	differential	理論値
L1	TOP	copper+plating	1.58 mils	4 mils, 50Ω±15% 8 mils, 40Ω±10% 10 mils, 35Ω±15%	53.94Ω 38.75Ω 34.22Ω	5/6/5 mils, 85Ω±10% 4/8/4 mils, 100Ω±10% 4/5/4 mils, 90Ω±10% 7/5/7 mils, 72Ω±10%	83.03Ω 97.18Ω 86.49Ω 70.72Ω
		Prepreg	3.50 mils				
L2		copper	1.30 mils				
		core	4.00 mils				
L3	IN1	copper	0.70 mils	4 mils, 50Ω±15% 8.5 mils, 40Ω±10% 10 mils, 35Ω±15%	56Ω 39.1Ω 35.68Ω	5/5/5 mils, 85Ω±10% 4/7/4 mils, 100Ω±10% 5/6/5 mils, 90Ω±10% 7.5/5/7.5 mils, 72Ω±10%	84.33Ω 98.09Ω 87.21Ω 70.39Ω
		Prepreg+core	36.00 mils				
L4	IN2	copper	0.70 mils	4 mils, 50Ω±15% 8.5 mils, 40Ω±10% 10 mils, 35Ω±15%	56Ω 39.1Ω 35.68Ω	5/5/5 mils, 85Ω±10% 4/7/4 mils, 100Ω±10% 5/6/5 mils, 90Ω±10% 7.5/5/7.5 mils, 72Ω±10%	84.33Ω 98.09Ω 87.21Ω 70.39Ω
		core	4.00 mils				
L5		copper	1.30 mils				
		Prepreg	3.50 mils				
L6	Bottom	copper+plating	1.58 mils	4 mils, 50Ω±15% 8 mils, 40Ω±10% 10 mils, 35Ω±15%	53.94Ω 38.75Ω 34.22Ω	5/6/5 mils, 85Ω±10% 4/8/4 mils, 100Ω±10% 4/5/4 mils, 90Ω±10% 7/5/7 mils, 72Ω±10%	83.03Ω 97.18Ω 86.49Ω 70.72Ω
Bottom side solder mask		1.00 mils					
TOTAL		60.16 mils					
		1.53 mm					

## SCREW HOLE PLACEMENT

內徑/外徑(T)/外徑(B)



## PCB SILK SCREEN

PCB color codes:

EVT (Red); Silkscreen (White)

DVT (Blue); Silkscreen (White)

PVT (Green); Silkscreen (Yellow)

Production (Green); Silkscreen (White)

HW\_PWRGD --> D713,G (PS\_ON#)

VCORE\_GD --> D721,G (VRM\_PWRGD)

FCH\_PWRGD --> D715,G (POWERGOOD\_140MS)

AUX\_POWER --> D720,G

SLP\_S3 --> D716,G

SLP\_S5 --> D717,G

AMD HDT --> JHDT1

LPC HEADER --> JLPADB1

ROM Recovery --> JHPDB5

BBW\_EN --> JHPDB4

USB WinDbg --> JUSBRDB1

DIMM0 --> JDDR52

DIMM1 --> JDDR92

T&I PCB Version = X01 , AX1 , AX2.....

MP = A01 , A02.....

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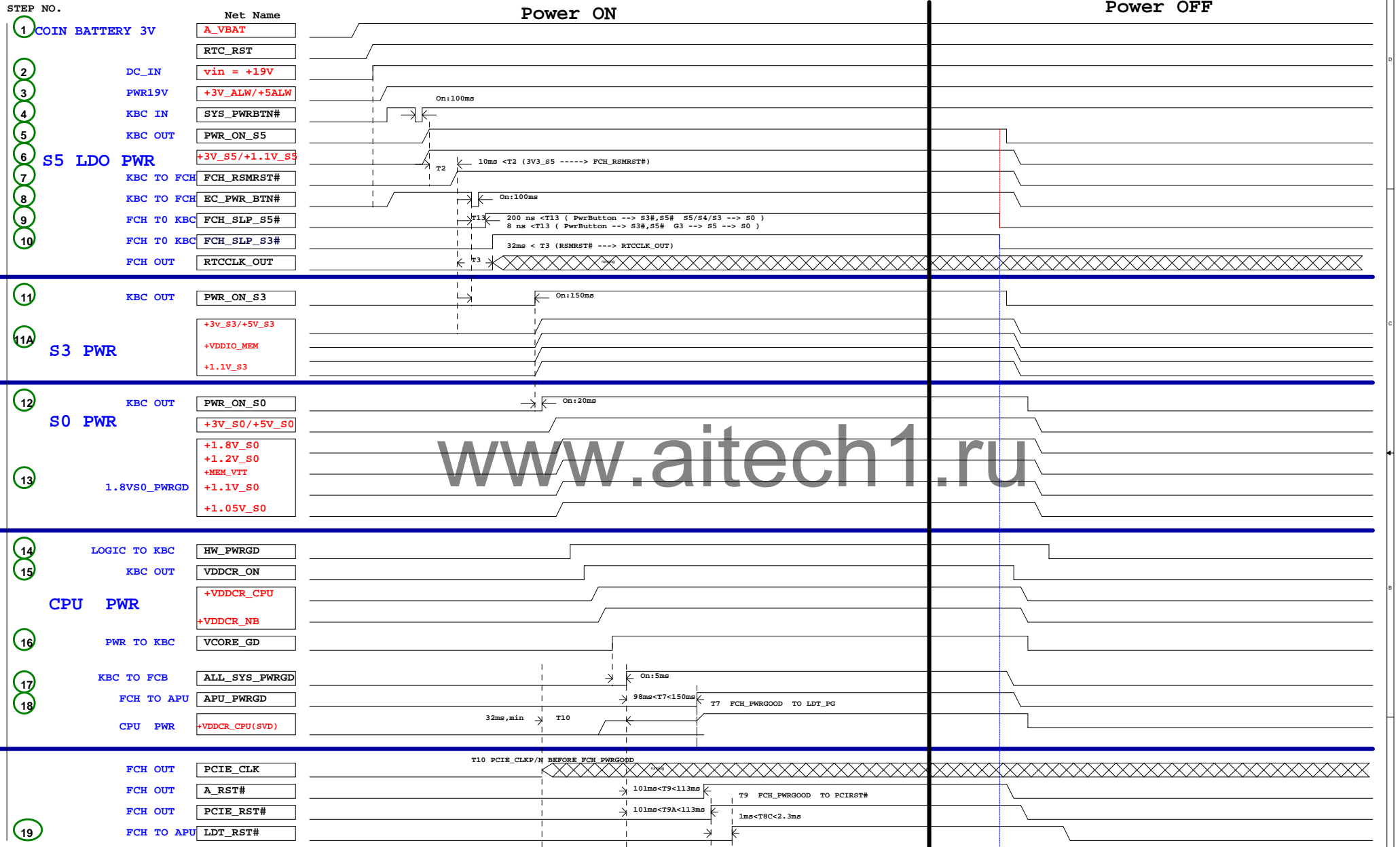


TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
1	TPV MODEL	Arroyo3	Rev	A02
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TPV (Top Victory Electronics Co., Ltd.)		OEM MODEL	HP_Arroyo3	Size	C
線路圖編號	1	TPV MODEL	Arroyo3	Rev	A02
Key Component	TABLE OF CONTENTS	PCB NAME	6050A2516401		
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# POWER SEQUENCE



TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
Key Component	TPV MODEL	Arroyo3	Rev	A02
Date	TABLE OF CONTENTS	PCB NAME	6050A2516401	備註 <備註>
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## Hudson D3L Fusion Controller HubGPIO Table

PIN NAME	PIN#	POWER WELL	USAGE	Default Type	Default Value	During Reset	81	83	84/85	Enable Setting	Note
GP100	A73	MAIN	NC	AD0	Output HIGH						
GP101	A73	MAIN	NC	AD1	Output HIGH						
GP102	A68	MAIN	NC	AD2	Output HIGH						
GP103	AL8	MAIN	NC	AD3	Output HIGH						
GP104	AL8	MAIN	NC	AD4	Output HIGH						
GP105	A75	MAIN	NC	AD5	Output HIGH						
GP106	AL1	MAIN	NC	AD6	Output HIGH						
GP107	A85	MAIN	NC	AD7	Output HIGH						
GP108	A86	MAIN	NC	AD8	Output HIGH						
GP109	A71	MAIN	NC	AD9	Output HIGH						
GP110	AL8	MAIN	NC	AD10	Output HIGH						
GP111	AL1	MAIN	NC	AD11	Output HIGH						
GP112	A87	MAIN	NC	AD12	Output HIGH						
GP113	AL6	MAIN	NC	AD13	Output HIGH						
GP114	A87	MAIN	NC	AD14	Output HIGH						
GP115	A88	MAIN	NC	AD15	Output HIGH						
GP116	A89	MAIN	NC	AD16	Output HIGH						
GP117	A81	MAIN	NC	AD17	Output HIGH						
GP118	A110	MAIN	NC	GP18	Output HIGH						
GP119	A113	MAIN	NC	AD19	Output HIGH						
GP120	A11	MAIN	NC	AD20	Output HIGH						
GP121	A82	MAIN	NC	AD21	Output HIGH						
GP122	A83	MAIN	NC	AD22	Output HIGH						
GP123	A812	MAIN	PCI_AD23	AD23	Output HIGH						
GP124	AC12	MAIN	PCI_AD24	AD24	Output HIGH						
GP125	A833	MAIN	PCI_AD25	AD25	Output HIGH						
GP126	AP13	MAIN	PCI_AD26	AD26	Output HIGH						
GP127	A813	MAIN	PCI_AD27	AD27	Output HIGH						
GP128	A814	MAIN	NC	AD28	Output HIGH						
GP129	AD19	MAIN	NC	AD29	Output HIGH						
GP130	AC16	MAIN	NC	AD30	Output HIGH						
GP131	A816	MAIN	SB_AD31	AD31	Output HIGH						
GP132	A718	MAIN	SDA, ARM, GATE	INT#B	HIGH						Internal PU 8.2K
GP133	A818	MAIN	NC	INT#B	HIGH						Internal PU 8.2K
GP134	A718	MAIN	NC	INT#B	HIGH						Internal PU 8.2K
GP135	AD18	MAIN	NC	INT#B	HIGH						Internal PU 8.2K
GP136	AP1	MAIN	PCI_CLK1_R	PC1CLK1	CLK						Output 33MHz
GP137	AP5	MAIN	PCI_CLK2_R	PC1CLK2	CLK						Output 33MHz
GP138	AP2	MAIN	PCI_CLK3_R	PC1CLK3	CLK						Output 33MHz
GP139	AP6	MAIN	PCI_CLK4_R	PC1CLK4	CLK						Output 33MHz
GP140	A813	MAIN	PCI_REQ0#	REQ0#	HIGH						24K QIO/Output 33MHz
GP141	AP15	MAIN	PCI_REQ2#	REQ2#	HIGH						Internal PU 15K
GP142	AP17	MAIN	PCI_REQ3#	REQ3#	HIGH						Internal PU 15K
GP143	AD26	MAIN	POR_SCL0	GP1	Tri-State						Internal PU 15K
GP144	AD13	MAIN	PCI_GNT#1	GNT#1	Output/HIGH						
GP145	AD21	MAIN	PCI_GNT#4	GNT#4	Output/HIGH						
GP146	AP17	MAIN	PCI_GNT#10	GP1	HIGH						Internal PU 8.2K
GP147	AD25	MAIN	POR_SDA0	GP1	Tri-State						
GP148	A819	MAIN	BR#10	GP1	HIGH						Internal PU 8.2K
GP149	AP27	MAIN	CLK_REQ0#	GP1	HIGH						Internal PU 8.2K
GP150	AP26	MAIN	PCIE_RST#_GATK	GP1	HIGH						Internal PU 8.2K
GP151	AD24	MAIN	PCI_DP10#1	GP1	HIGH						Internal PU 8.2K
GP152	AP16	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP153	A815	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP154	A118	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP155	A817	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP156	A818	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP157	A816	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP158	AL16	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP159	A818	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP160	AP22	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP161	AD22	MAIN	PCIE_LAN_CLKREQ0#	GP1	HIGH						Internal PU 8.2K
GP162	AD23	MAIN	PCIE_CLKREQ2	GP1	HIGH						Internal PU 8.2K
GP163	AD24	MAIN	PCIE_CLKREQ2_WLAN#	GP1	HIGH						Internal PU 8.2K
GP164	AD24	MAIN	CLK_REQ4#	GP1	HIGH						Internal PU 8.2K
GP165	AP23	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP166	AD24	MAIN	PCIE_BEEP	GP1	Tri-State						Internal PU 8.2K
GP167	AD22	MAIN	SATA_LED#	GP1 (OD)	Tri-State						Internal PU 8.2K
GP170	A114	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP174	AD4	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP175	A114	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP176	A811	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP177	A813	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP178	A815	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP179	A815	MAIN	NC	GP1	HIGH						Internal PU 8.2K
GP180	A114	MAIN	NC	GP1	HIGH						Internal PU 8.2K

### Hudson D3L Fusion Controller HubGPIO Table

[illegible]



Schematic Modify History

X01 --> AX1   Modify date 2012/2/24~2012/4/16  
(2012/2/10 gerber out for EVT)

- 1.Change webcam port from port6 to port8.
- 2.Change RJ45 CONN P/N to 6026B0229101.
- 3. Add nc/ mark into part id of properties of the NC part.
- 4. Page 19 Add JHPD5 Jump P/N,記得MP後,拿掉包括header ,然後R308要上件,
- 5. Page 9   ADD JRTCCLR1-jump
- 6. Page 19   ADD JHPDB1-jump
- 7. Page 6,7   change C249,C265 to 1uf and add c317 C320 1 uf For vrefca 突波
- 8. Page 14 R278 249ohm change to 2.49k for Lan can't link issue.
- 9.page 8 remove edid component
- 10.page 19 co-lay mini pci 1.2 power solution
- 11.page 8 C1 10uf change to 47uf for falling time 下降太快
- 12.page 9   C123 8pf change to 10pf for RTC fail (page 1 RTC change list has been change)
- 13.page 10 remove R158,R213,R154,Q9,Q12,add R159 R176 for SCLK0 undershoot issue.
- 14.(power modify)page 22 change PR25 20k to 1k and add PC157 & PC77   for +1.2v waveform
- 15.page 10 bios request add R150 to 0ohm R143 & R146 to 10k
- 16.page 16 add 2 cap for EMI solution
- 17.page 19 add JPHDB3 for bios request
- 18.page 14 R12 R8 from 220ohm to 33ohm for LED over dark problem

AX1 -->A01   Modify date 2012/4/16 ~ 2012/5/22  
(2012/5/22 gerber out for PVT)

- 1.page 20 PC60 to 1uf,PR59 to 1k for +5v\_s0 drop issue
- 2.page 14 c105 c107 change to 12pf for vendor suggestion
- 3.page 21 del   PU11,PC148,PC152,PC155,PC156,PR182,PR185,PR186,PC147 I-LIMIT function disable
- 4.page 24 PR4,PR28,PR104,PR114,PR115,PR116,PR124,PR126 0 ohm change to default short
- 5.page 9 change R184 from R0402\_short to R0402 footprint
- 6.page 1 NPI request increase optical point
- 7.page 26 change hole footprint to NP from H5 to H8

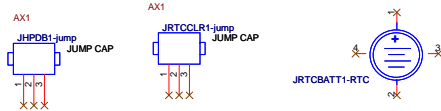
A01 -->A02   Modify date 2012/6/21 ~ 2012/7/11  
(2012/7/16 gerber out for MP)

- 1.page 19 removed Debug circuit   R310,JLPCDB1,L1,L2,L3,C14,C15,C16,R28,R29,R30,U1,U2,R10,R11,Q1,Q2,C3,C4,R1,R2,D23,JCRTDB1,D22,R53,R54,D9,D10,D11,D12.D13,D14,U31,Q22,R312,R313,R314,R315,R316,R317,Q22,C21,C22,C7,R25,R26
- 2.page 19   because remove jump cap so ADD R308 to short SPI CS#
- 3.page 22   because anx3110 timing sequence removed PR25,add PR1 to 1k
- 4.page 16 web cam display issue. AMD recommand remove R532 & C390
- 5.page 14 vendor suggest removed R246 & R227. because Lan IC has been build circuit inside.

PVT & MP de-pop circuit

- page 18 for EC debug connector  
JECDB1
- page 19 for HP debug  
JLPCDB1,R310,JHPDB5,C147,U31,Q22,D9~D14,R312~R317
- page 19 for AMD debug  
U5,R53,R56,R77,R80
- page 19 for TnI HW debug (CRT output function)  
C3,C4,C7,C14,C15,C16,C21,C22,D22,D23,JCRTDB1  
L1,L2,L3,Q1,Q2,R1,R2,R3,R4,R5,R6,R10,R11,R14  
R15,R25,R26,R28,R29,R30,R32,R36,R37,U1,U2

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